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COWAN LIEBOWITZ & LATMAN

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02/25/2005

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Eugene L. Flanagan III (Depositor's name)
Eugene L. Flanagan III (Signature)
May 25, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/748,580	12/30/2003	Tsin-Yuan Chang	03901-P0004A	8240

TITLE OF INVENTION: METHOD AND APPARATUS FOR CONTROLLING A DUAL-SLOPE INTEGRATOR CIRCUIT TO ELIMINATE SETTLING TIME EFFECT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	05/25/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
WELLS, KENNETH B	2816	327-344000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Cowan, Liebowitz & Latman, P.C.

2 Eugene L. Flanagan III

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

National Tsing Hua University

Taiwan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(a)(2).

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Authorized Signature

Eugene L. Flanagan III

Date May 25, 2005

Typed or printed name

Eugene L. Flanagan III

Registration No. 27,634

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Re: Application No. 10/748,580
Filing Date: December 30, 2003
Applicant: Tsin-Yuan Chang et al
For: METHOD AND APPARATUS FOR CONTROLLING A DUAL-SLOPE
INTEGRATOR CIRCUIT TO ELIMINATE SETTLING TIME EFFECT
Our Ref: 25904.004

To: U.S. Patent and Trademark Office**Fax No: 703-746-4000****Tel No:****From: Eugenc L. Flanagan****Date: May 25, 2005****No Confirmation Copy****No. of Pages (Incl. Cover): 2****Message:**

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